

Fig. 1b
(Prior art)

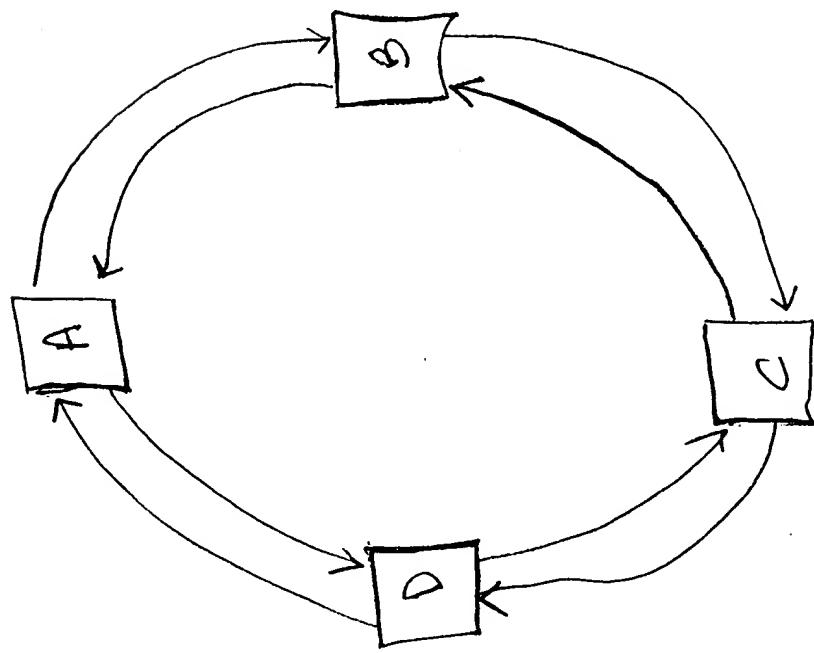


Fig. 1a
(prior art)

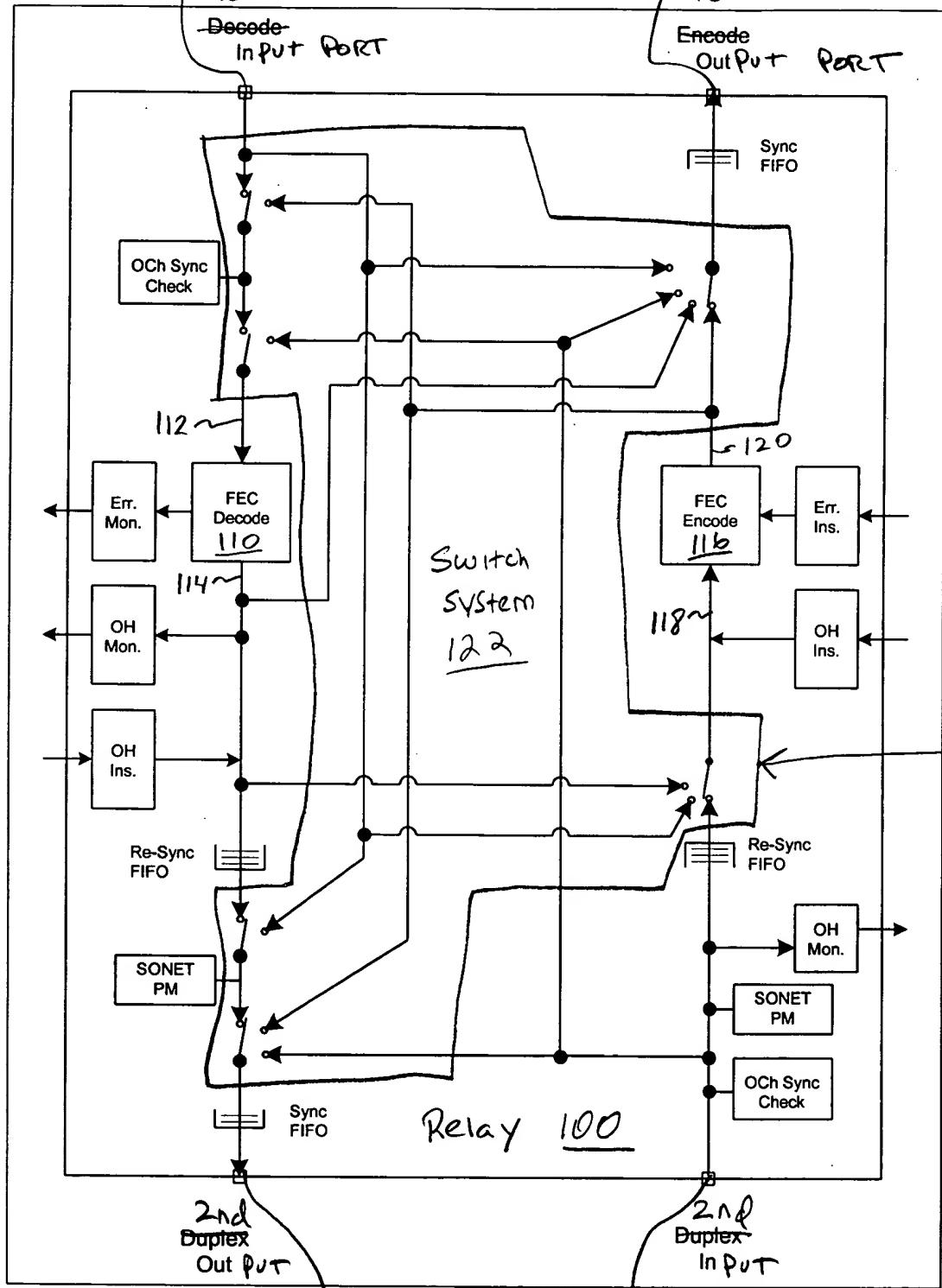


Fig. 2

1st mode

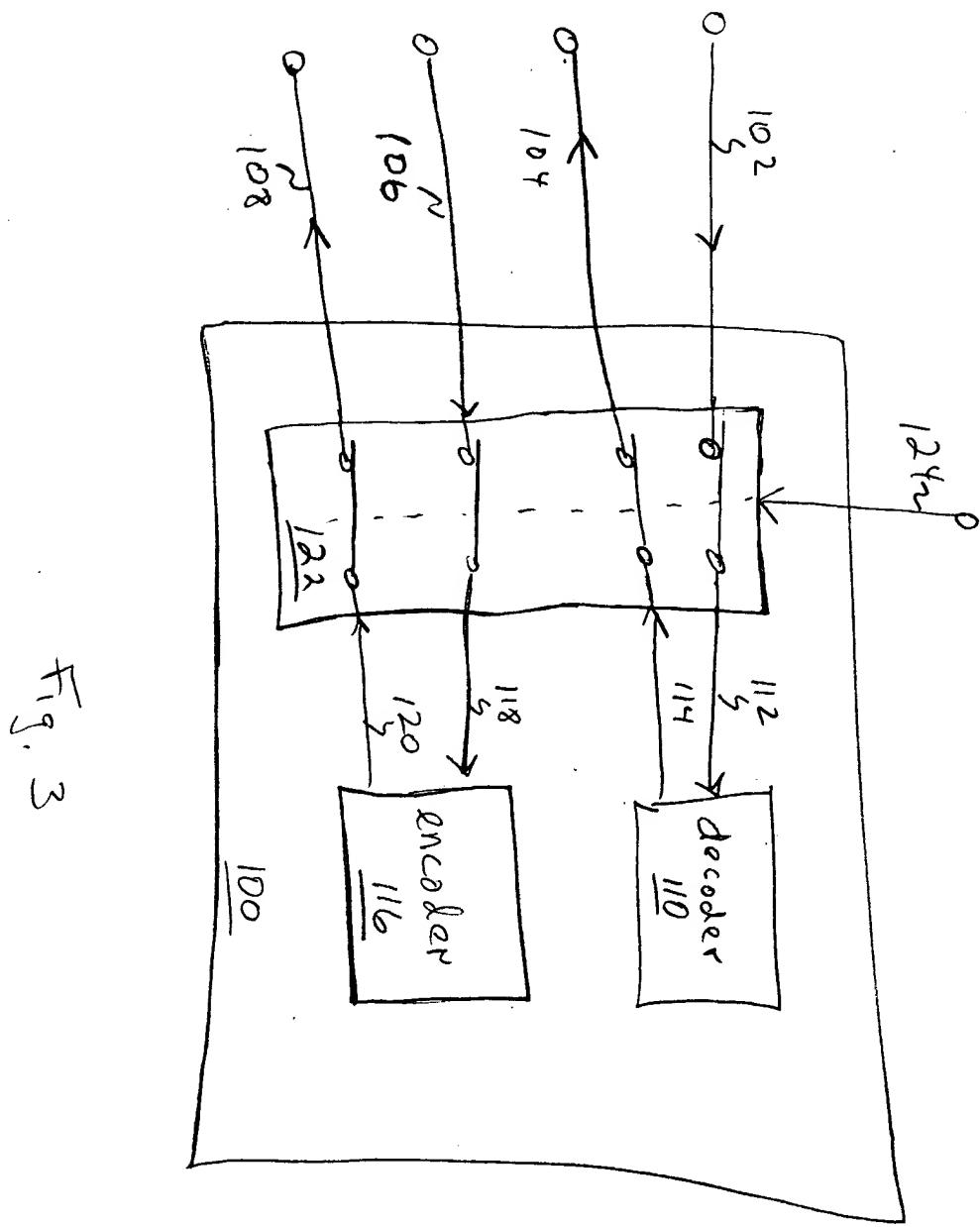
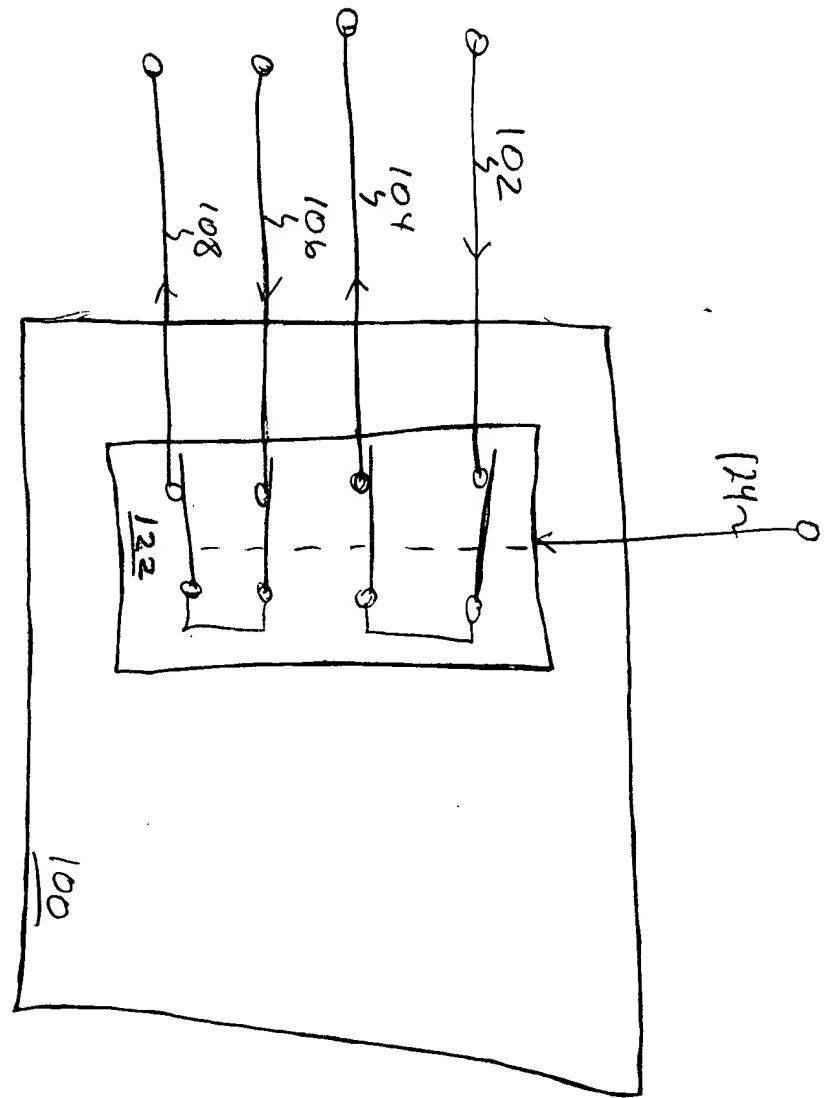


Fig. 3

Fig. 4



2nd Mode

3rd Mode

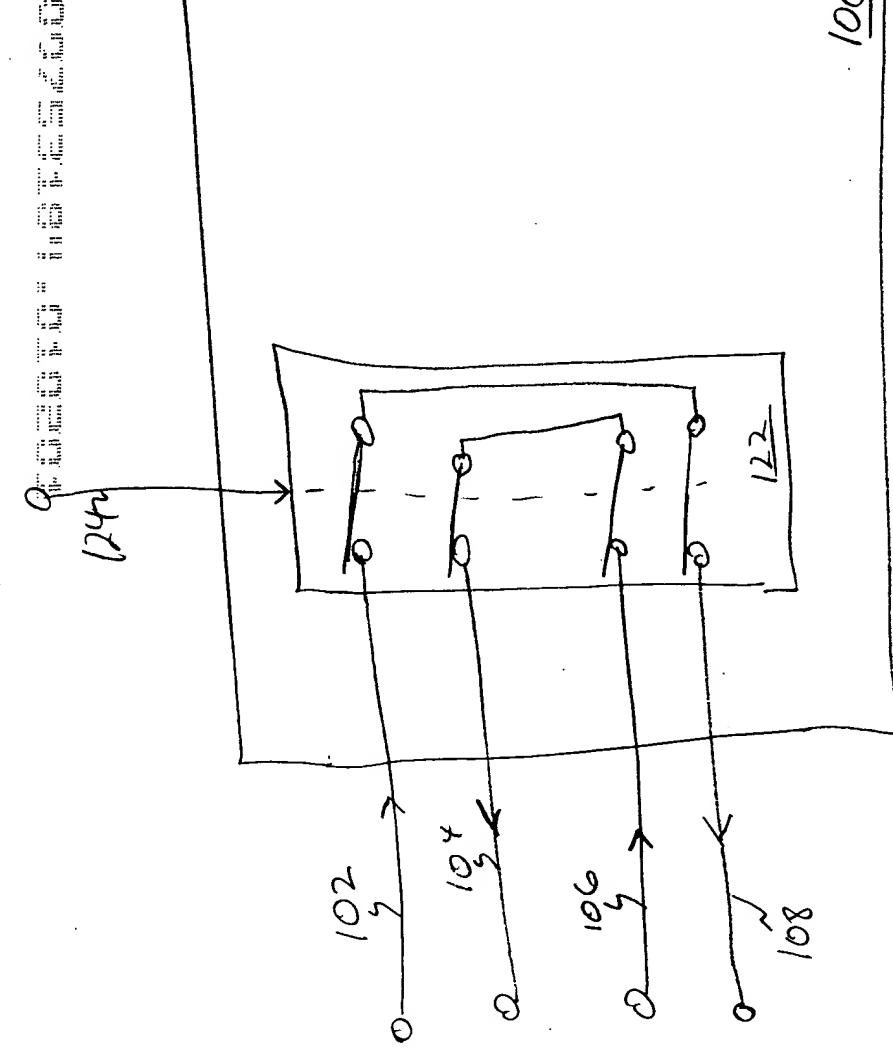


Fig. 5

4th Mode

1242

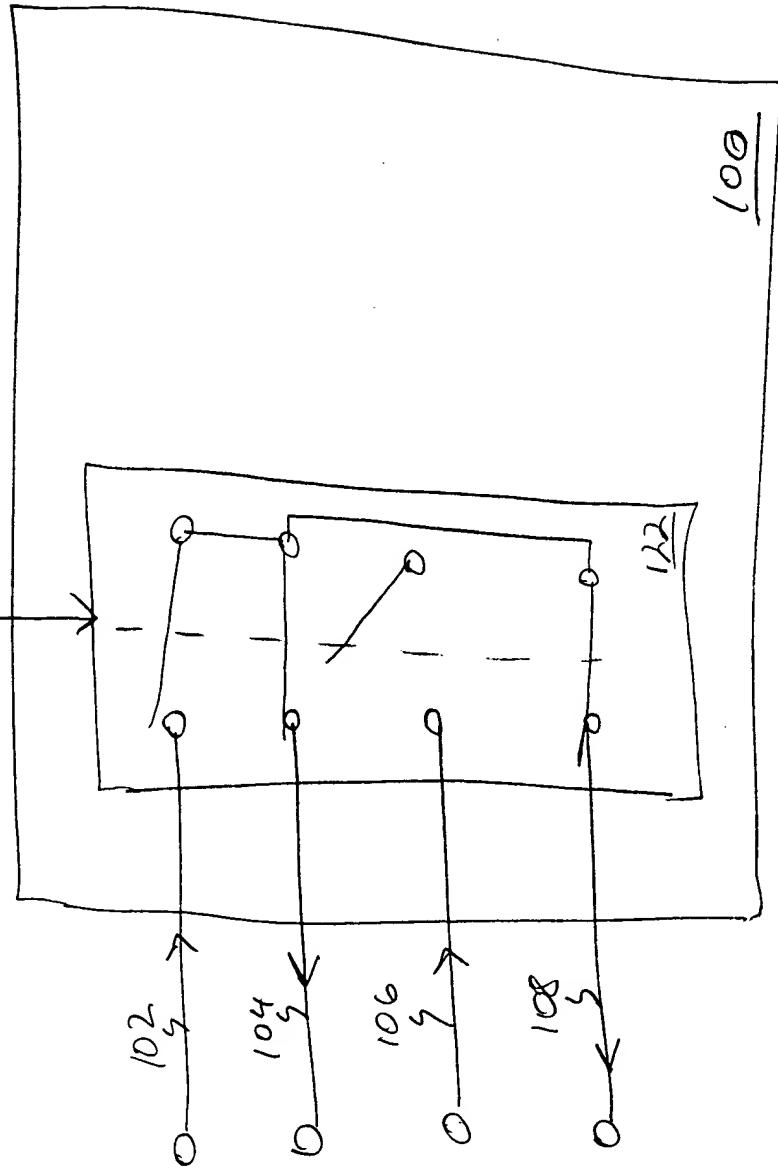


Fig. 6

5th mode

124 μ

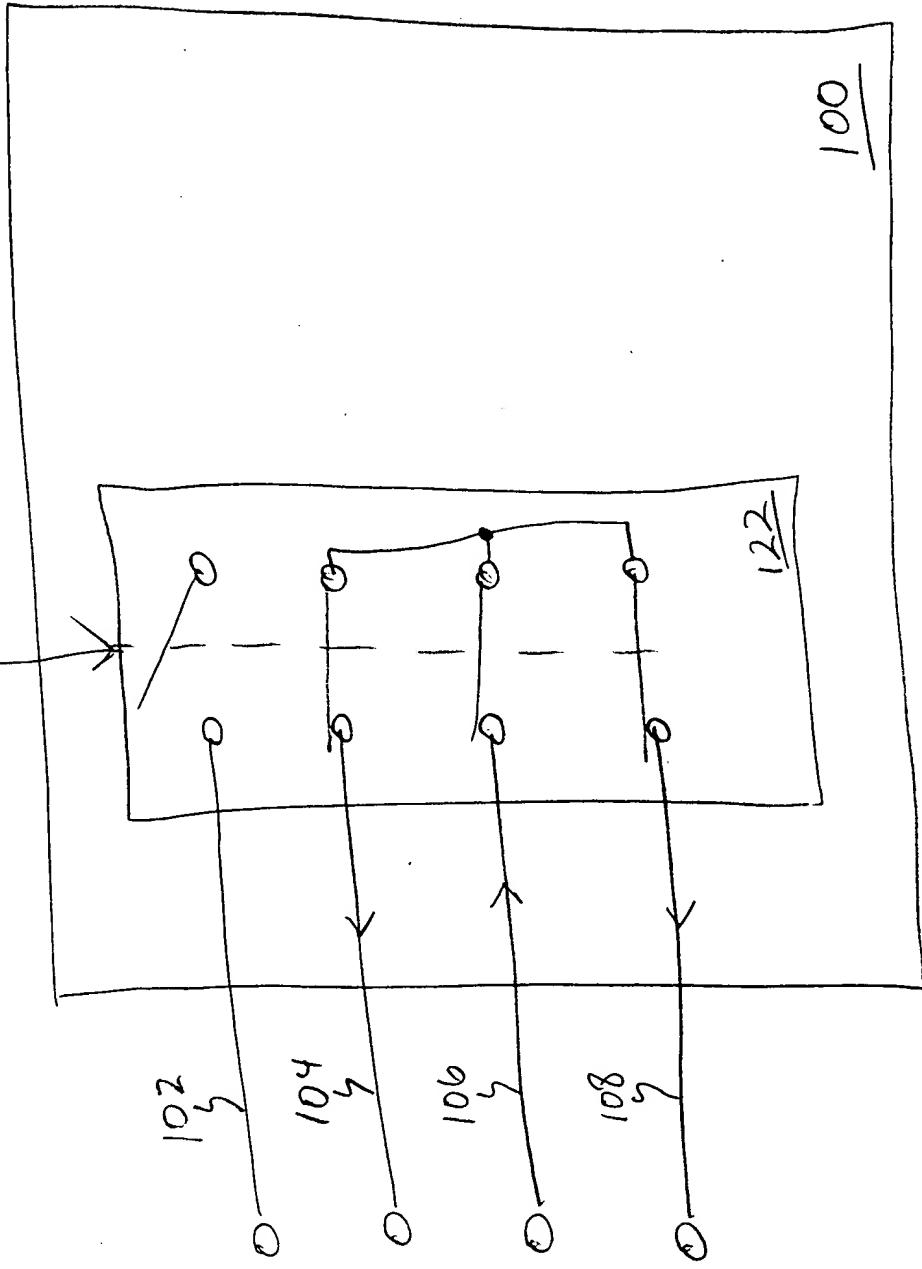


Fig. 7

6th mode

124~

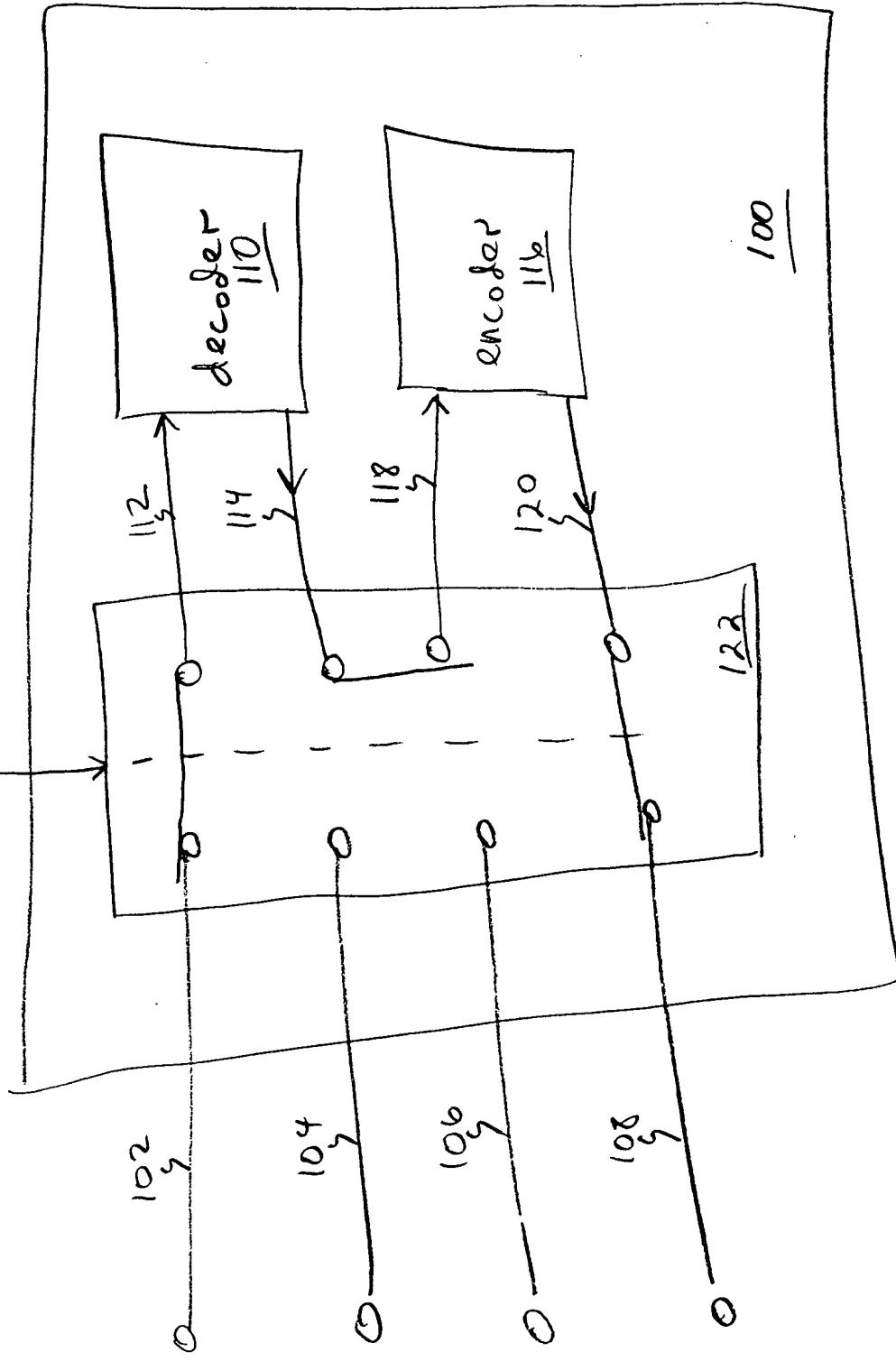


Fig. 8

7th Mode

1242

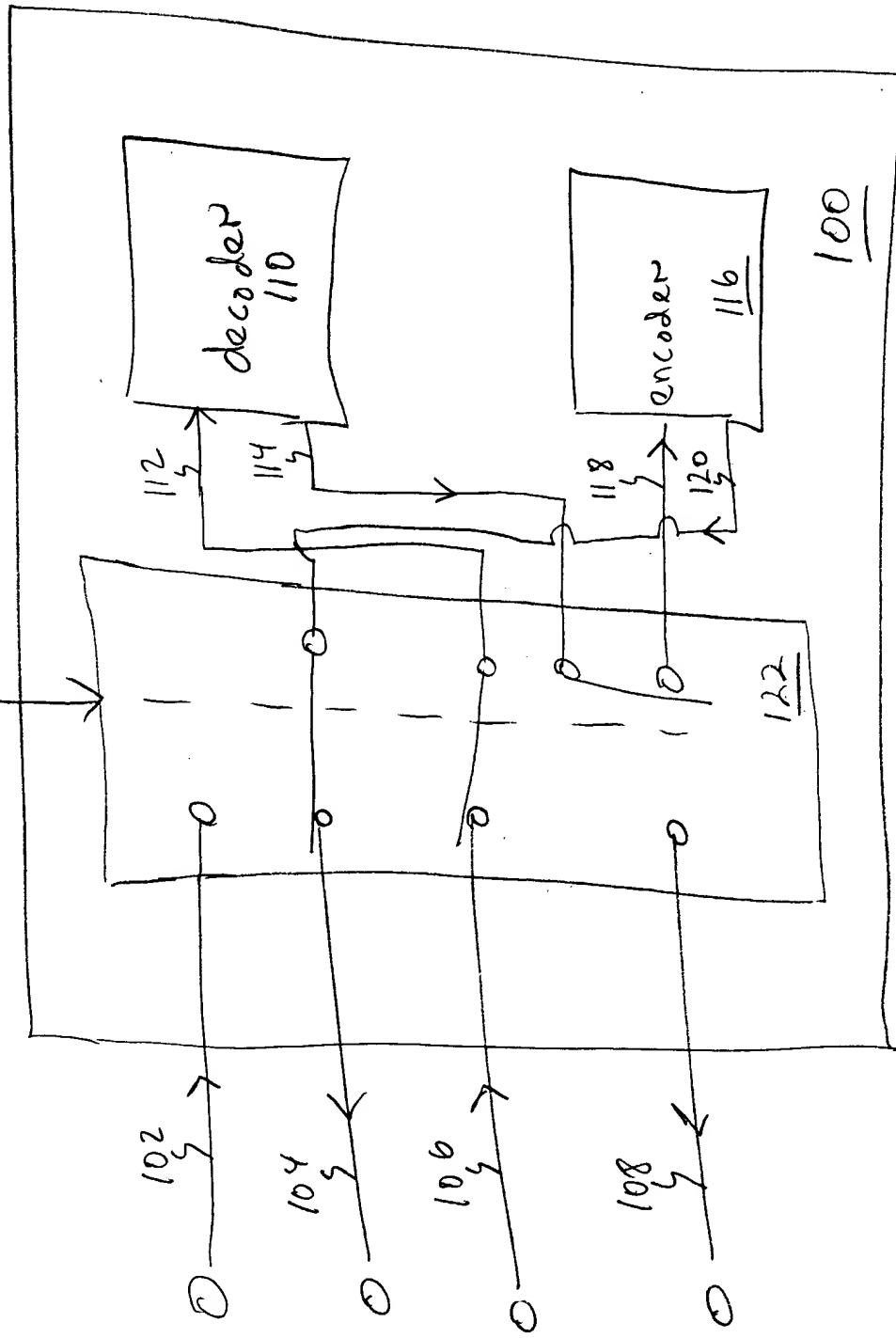


Fig. 9

8th mode

0 124
0 123

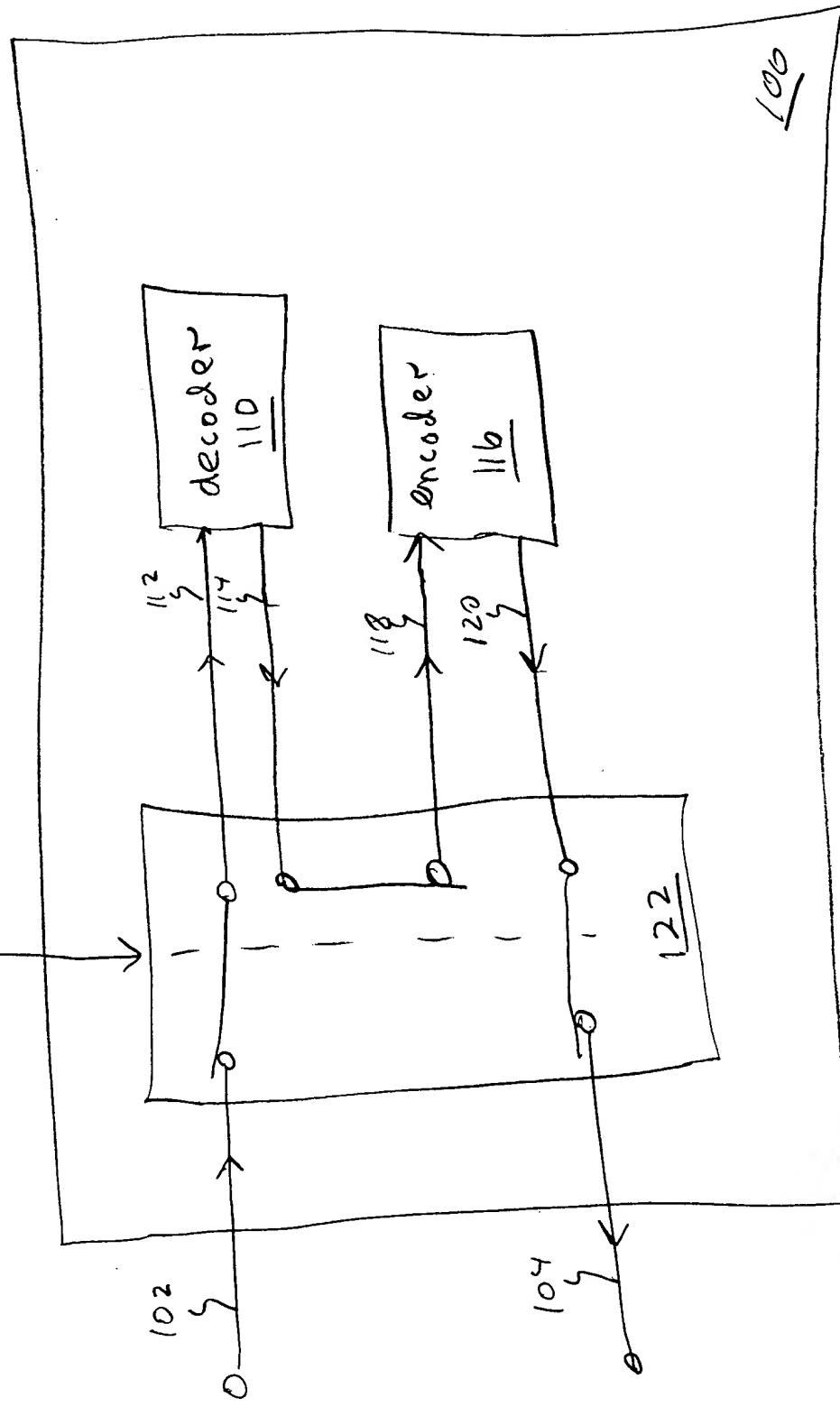


Fig. 10

10th mode

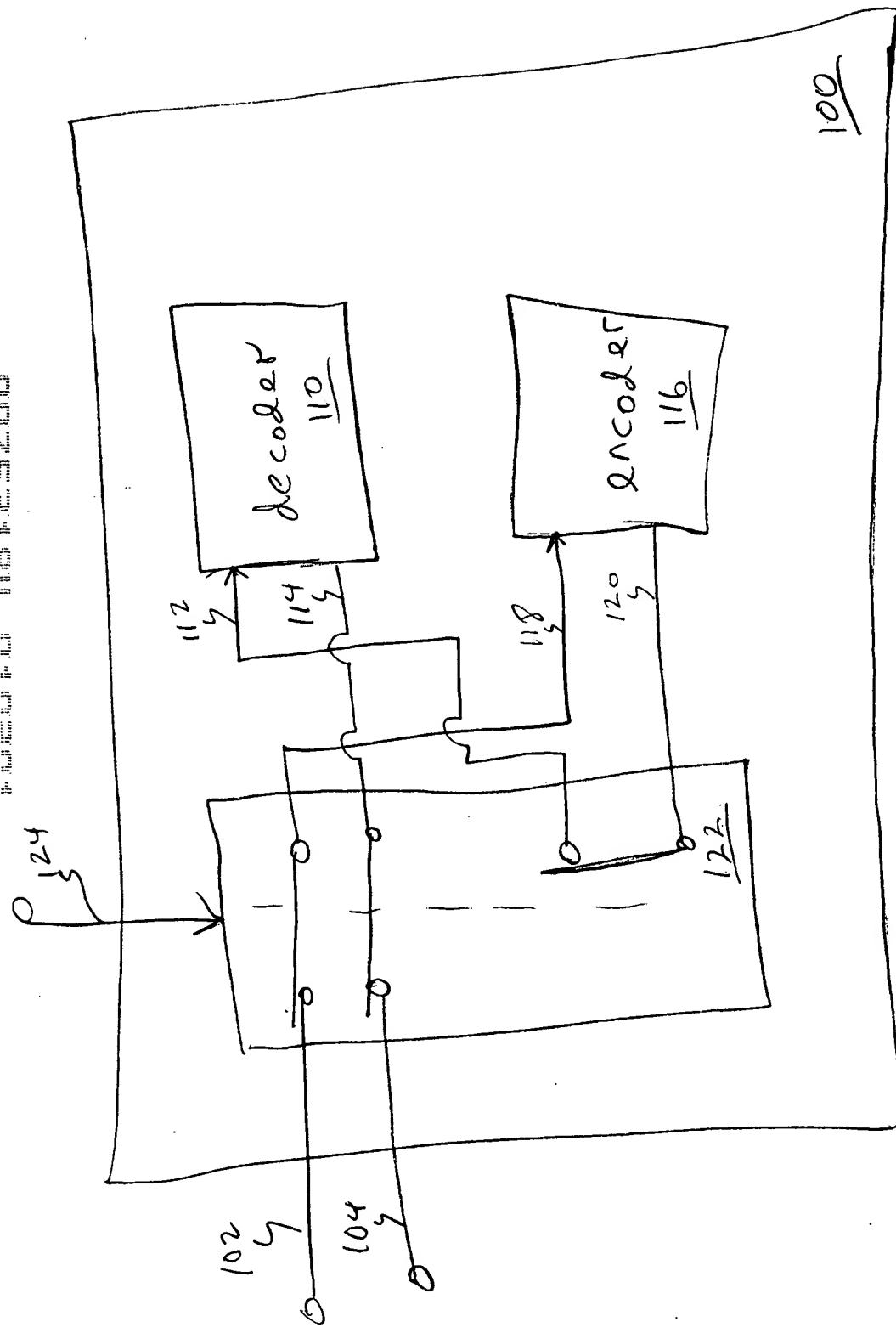


Fig. 11

Fig. 12 shows a block diagram of a digital communication system in Rician mode.

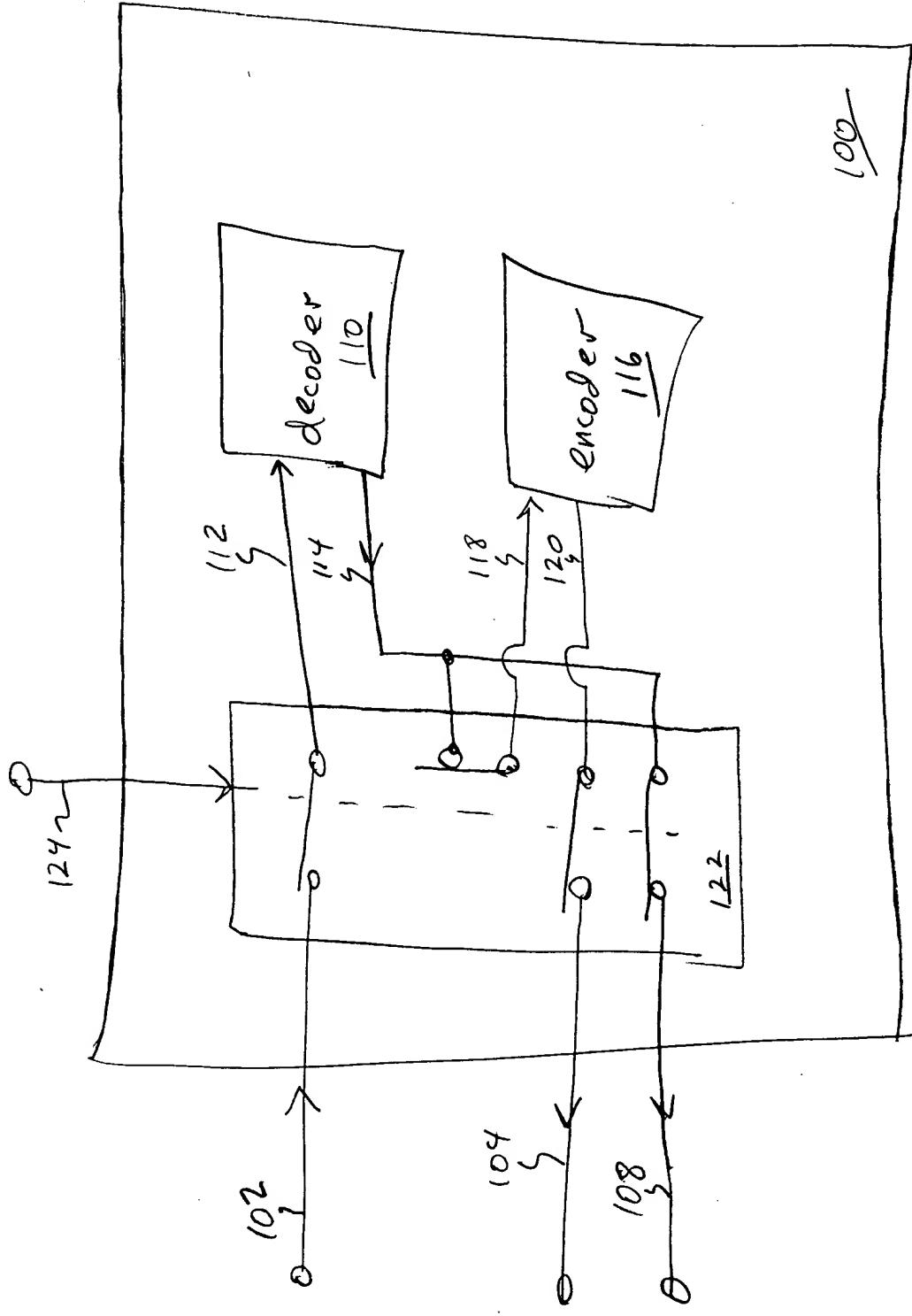


Fig. 12

Fig. 13
12th Mode

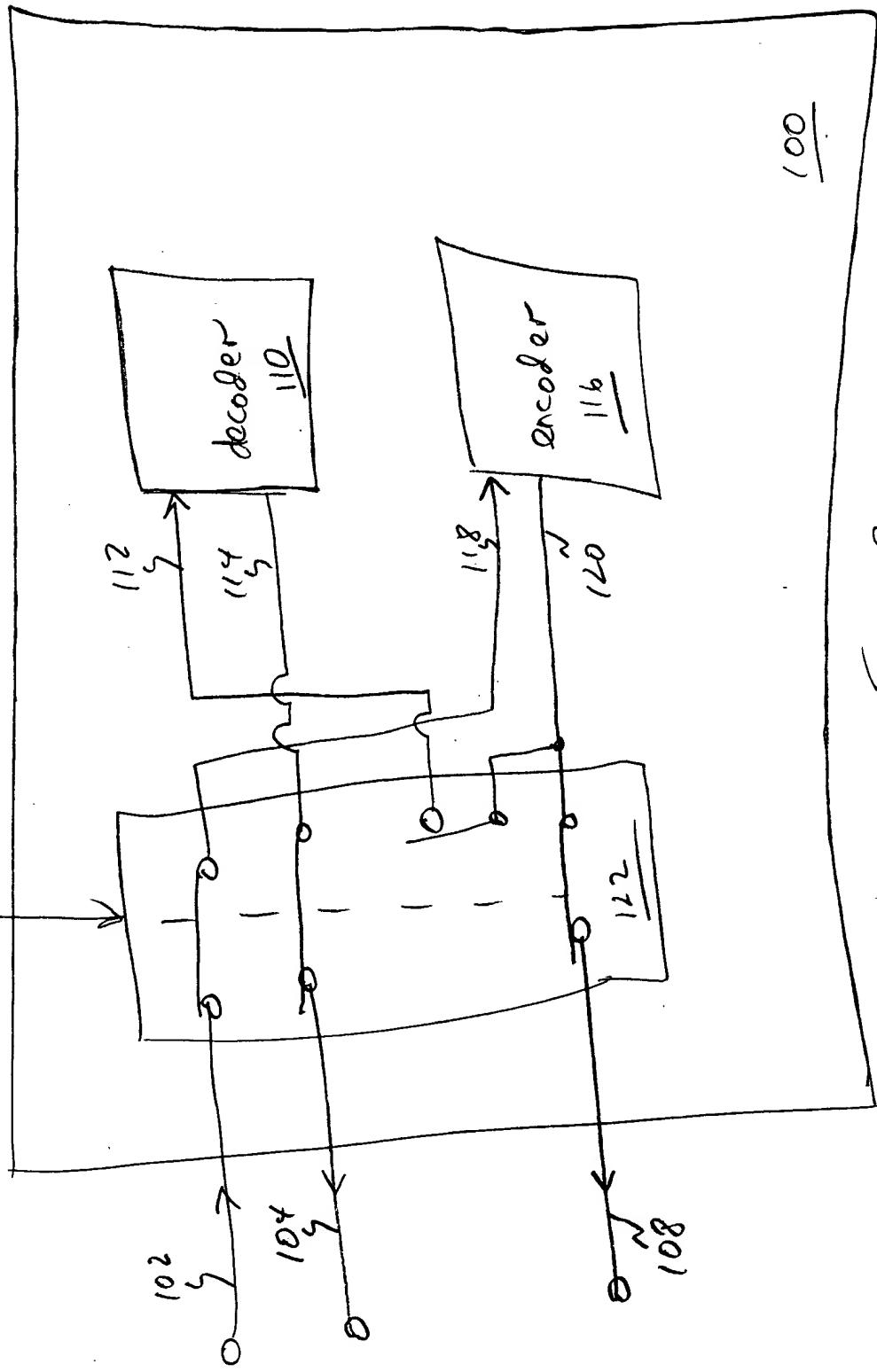


Fig. 13

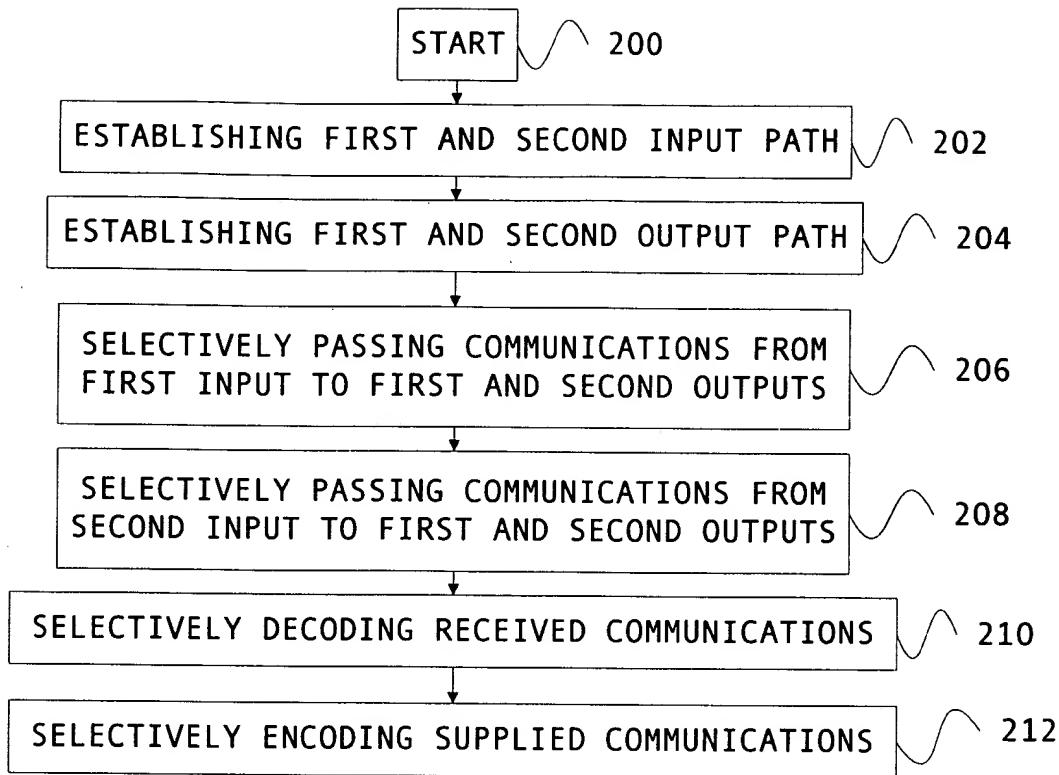


FIG. 14

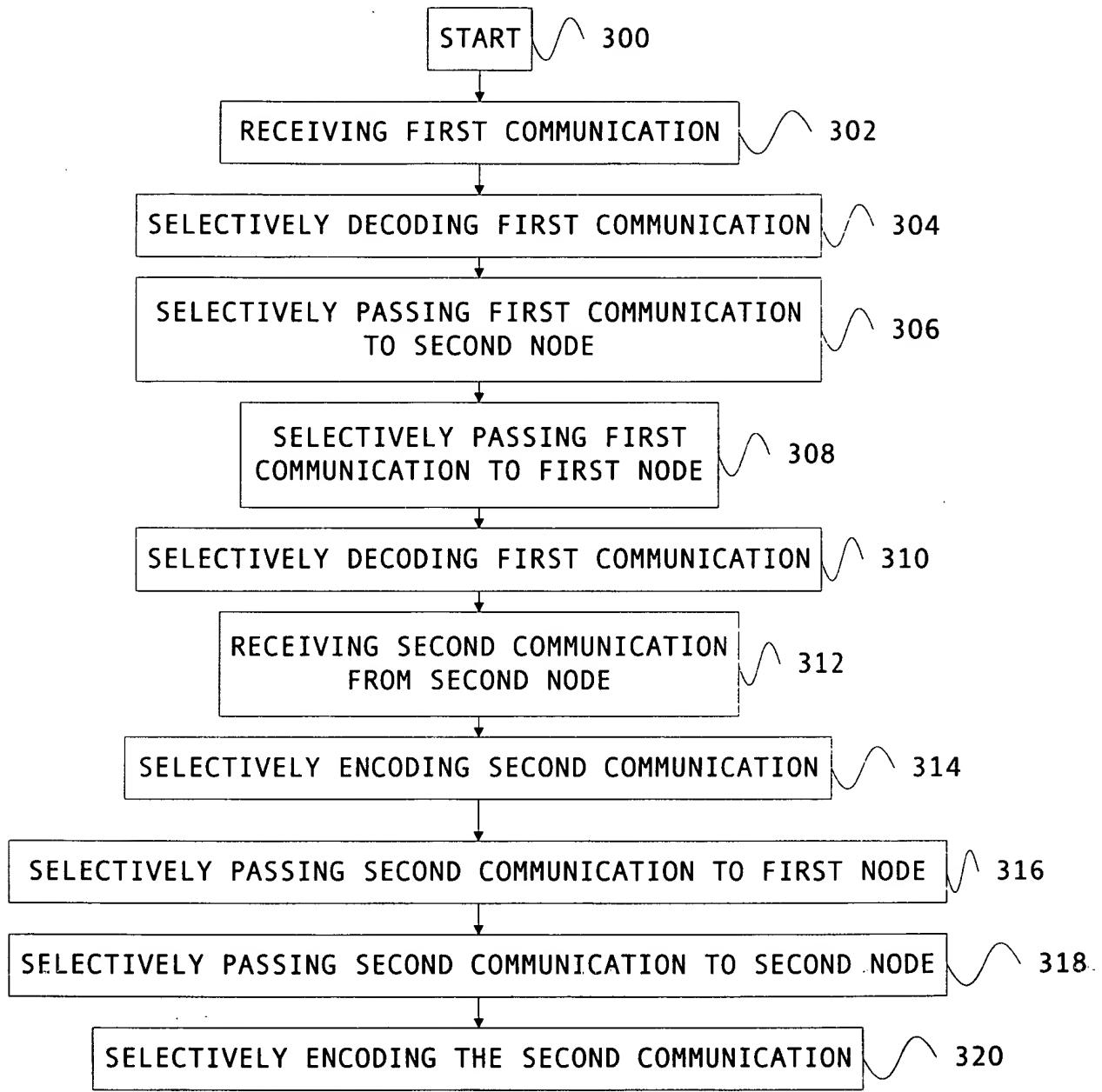


FIG. 15